

Substitute for form 1449A/PTO			<b>Complete if Known</b>		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>			Application Number	10/757,925	
			Filing Date	January 16, 2004	
			First Named Inventor	Craig C. HANSEN, et al.	
			Group Art Unit	2181	
			Examiner Name	TSAL HENRY	
Sheet	1	of	4	Attorney Docket Number	43876-158

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US-3,814,924	06-04-1974	Tate	
		US-4,825,401	04-19-1989	Ikumi	
		US-4,953,119	08-28-1990	Wong	
		US-5,187,796	02-16-1993	Wang	
		US-5,390,135	02-14-1995	Lee	
		US-5,442,799	08-15-1995	Murakami	
		US-5,448,509	09-05-1995	Lee	
		US-5,467,131	11-14-1995	Bhaskaran	
		US-5,477,543	12-19-1995	Purcell	
		US-5,521,856	05-28-1996	Shiraishi	
		US-5,586,070	12-17-1996	Purcell	
		US-5,590,350	12-31-1996	Gutttag	
		US-5,673,321	09-30-1997	Lee	
		US-5,819,101	10-06-1998	Peleg	
		US-5,872,965	02-16-1999	Petrack	
		US-5,881,275	03-09-1999	Peleg	
		US-5,883,824	03-16-1999	Lee	
		US-6,058,465	05-02-2000	Nguyen	
		US-6,381,690	04-30-2002	Lee	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number 4- Kind Code <sup>5</sup> (if known)				
		WO 93/01543	01-21-1993	Garg et al.		

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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
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		<b>MU v. SCEA</b> 2/14/06 SCEA's Answer, Affirmative Defenses, and Counterclaim to MicroUnity's Original Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
		7/11/07 P.R. 4-3 Joint Claim Construction Statement and supporting evidence ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		8/24/07 MU's LRP 4-5(a) Opening Brief on Claim Construction, and Exhibits 1-14 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		9/12/07 SCEA's Responsive Brief Regarding Claim Construction Pursuant to P.R. 4-5(b), and Exhibits 1-34 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		9/17/07 LPR 4-5(c) Reply Brief on Claim Construction from MicroUnity Systems Engineering, Inc., and Exhibits 15 and 16 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		9/20/07 Transcript of Claim Construction Hearing Before the Honorable T. John Ward United States District Judge ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		10/15/07 SCEA's Motion for Partial Summary Judgment of Invalidity for U.S. Patent Nos. 6,643,765 and 6,725,356, and Proposed Order, and Exhibits A-U ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		10/29/07 MU's Response Brief in Opposition to Sony's Motion for Partial Summary Judgment of Invalidity of U.S. Patent Nos. 6,643,765 and 6,725,356, and Proposed Order ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		10/29/07 Declaration of Michael Heim in Support of MU's Response Brief in Opposition to Sony's Motion for Partial Summary Judgment of Invalidity of U.S. Patent Nos. 6,643,765 and 6,725,356, and Exhibits A-P ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		11/6/07 Order Granting Joint Motion to Stay Litigation Pending Settlement ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		12/12/07 Order of Dismissal with Prejudice and Final Judgment ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> , 2:05-cv-505, U.S.D.C., E.D. Texas, Marshall Division)	
		<b>MU v. AMD</b> 8/6/07 AMD's Invalidity Contentions Under Patent Rule 3-3, and Exhibits A-I ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	
		9/5/07 AMD's Supplemental and Consolidated Invalidity Contentions - LPR 3-3, and Exhibits A-L ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	

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		1/8/08 Order of Dismissal With Prejudice and Final Judgment ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> , 2:06-cv-486, U.S.D.C., E.D. Texas, Marshall Division)	
		<b>MU v. DELL &amp; INTEL</b> 2/25/05 Defendant Dell Inc.'s Amended Answer, Affirmative Defenses, and Counterclaims to Plaintiff's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		2/25/05 Defendant Intel Corporation's Amended Answer, Affirmative Defenses, and Counterclaims to Plaintiff's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		4/11/05 MicroUnity Systems Engineering, Inc.'s Opening Brief Regarding Claim Construction Pursuant to Patent Local Rule 4-5(a) and Exhibits A-I ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		4/26/05 Supplement to Plaintiff MicroUnity Systems Engineering, Inc.'s Opening Brief Regarding Claim Construction (submitting a corrected page 41) ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		5/12/05 Dell, Inc. and Intel Corporation's Responsive Brief Regarding Claim Construction Pursuant to Patent Local Rule 4-5(b) ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		5/25/05 MicroUnity's Reply Brief Regarding Claim Construction Pursuant to Patent Local Rule 4-5(c) ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		6/9/05 Intel and Dell's Surreply Brief Regarding Claim Construction ( <i>MicroUnity Systems Engineering, Inc. v. Dell, Inc. f/k/a/ Dell Computer and Intel Corporation</i> ; C.A. No. 2-04-cv-120; U.S.D.C., E.D. Texas, Marshall Division)	
		Alpha Architecture Reference Manual," Digital Equipment Corporation (1992)	
		The AMD-K6 3D Processor: Revolutionary Multimedia Performance, Ed. H. Kalish and J. Isaac, Abacus (1998)	
		Araki et al., "The Architecture of a Vector Digital Signal Processor for Video Coding," IEEE (1992)	
		BIT Data Sheet - Product Summary: B3110/B3120/B2110/B2120 Floating Point Chip Set, Bipolar Integrated Technology, Inc. (Dec. 1986)	
		Borgerson et al., "The Architecture of the SPERRY UNIVAC 1100 Series System," IEEE (1979)	
		Buchholz, "The IBM System/370 Vector Architecture," IBM Systems Journal, Vol. 25, No. 1, 1985	
		Case, "First Trimedia Chip Boards PCI Bus," Microprocessor Report (Nov. 13, 1995)	
		Case, "Low-End PA7100LC Adds Dual Integer ALUs," Microprocessor Report (Nov. 19, 1992)	
		Case, "Phillips Hopes to Displace DSPs with VLIW," Microprocessor Report (Dec. 5, 1994)	
		Colwell et al., "A VLIW Architecture for a Trace Scheduling Compiler," <i>IEEE Transactions on Computers</i> , vol.37, no.8, 967-79 (Aug. 1988)	
		Colwell et al., "Architecture and Implementation of a VLIW Supercomputer," IEEE, 910-19 (1990)	
		Connection Machine CM-5 Technical Summary," Thinking Machines Corp. (November 1993)	

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		Eijndhoven et al., "TriMedia CPU64 Architecture," Int'l Conference on Computer Design (October 1999)	
		Enterprise Systems Architecture/390: Vector Operations," IBM Corp., First Edition (September 1991)	
		Epstein, "Chromatic Raises the Multimedia Bar," Microprocessor Report (Oct. 23, 1995)	
		Foley, "The Mpaact™ Media Processor Redefines the Multimedia PC," <i>Proceedings of COMPCON Spring '96</i> , IEEE, 311-18 (1996)	
		Gibson et al., "Engineering and scientific processing on the IBM 3090," IBM Systems Journal, Vol. 25, No. 1, 1986	
		Gwennap, "UltraSPARC Adds Multimedia Instructions," Microprocessor Report (Dec. 5, 1994)	
		Gwennap, "UltraSPARC Unleashes SPARC Performance," Microprocessor Report (Oct. 3, 1994)	
		Gwennap, "PA-8000 Combines Complexity and Speed," Microprocessor Report (November 14, 1994)	
		Hwang et al., <i>Computer Architecture and Parallel Processing</i> , McGraw Hill Book Co., Singapore (1988) (7 pages)	
		IEEE Standard for Scalable Coherent Interface (SCI), Std. IEEE1596-1992, © 1993	
		Intel Architecture MMX™ Technology: Programmer's Reference Manual," Intel Corp., (March 1996)	
		"Intel MMX™ Technology Overview," Intel Corp. (Mar. 1996)	
		Kalapathy, "Hardware-Software Interactions on Mpaact," IEEE Micro (1997)	
		Kimura et al., "Development of Gmicro 32-bit Family of Microprocessors, Fujitsu Semiconductor Special Collection," Vol. 43, No. 2, pp. 89-97 (February 1992)	
		Lang et al., "An Optimum Parallel Architecture for High-Speed Real-Time Digital Signal Processing," IEEE (February 1988)	
		Lowney et al., "The Multiflow Trace Scheduling Compiler," (Oct. 30, 1992)	
		MDMX Digital Media Extension," MIPS	
		MIPS R4000 Microprocessor User's Manual: Second Edition," MIPS Technologies, Inc. (1994)	
		Moore et al., "Concepts of the System/370 Vector Architecture," ACM (1987)	
		Mpaact Media Processor: Preliminary Data Sheet," Chromatic Research, Inc. (September 11, 1996)	
		Padegs et al., "The IBM System/370 Vector Architecture: Design Considerations," IEEE (1988)	
		PA-RISC 2.0 Architecture and Instruction Set Reference Manual, Hewlett-Packard (1995)	
		Rathnam et al., "An Architectural Overview of the Programmable Multimedia Processor, TM-1," <i>Proceedings of COMPCON '96</i> , IEEE (Spring 1996)	
		Rubinfeld, et al., "Motion Video Instruction Extensions for Alpha," Semiconductor Eng'g Group (Oct. 18, 1996)	
		Shanley, Pentium Pro Processor System Architecture, MindShare, Inc., Addison-Wesley Developers Press (1997)	
		The SPARC Architecture Manual: Version 9," SPARC International, Inc. (1994)	
		TM1000 Preliminary Data Book, TriMedia Product Group (1997)	
		"TRACE /300 Series: F Board Architecture," Multiflow Computer (Dec. 9, 1988)	
		Tucker, "The IBM 3090 System: An Overview," IBM Systems Journal, Vol. 25, No. 1, 1986	
		The Vector Coprocessor Unit (VU) for the CM-5," Hot Chip IV Symposium (August 11, 1992)	
		VIST™ Instruction Set User's Manual," Sun Microsystems, Inc. (May 2001)	
		Extended European Search Report dated 2/18/08 re Application No./Patent No. 07111351.8-1243	

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